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Helion Technology Job Description
July 2012

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Company profile

Helion Technology is a successful specialist engineering business developing and licensing embedded encryption and lossless data compression IP into the FPGA and ASIC markets.

The company today comprises a small team of experienced engineers, and has been self-financed and profitable throughout its 14 year history. It serves a worldwide customer base from its headquarters in Fulbourn, Cambridge, and is an accredited partner of industry giants such as Xilinx.

The company is now seeking new recruits to satisfy an increasing demand for its current product portfolio and to develop products for expansion into new applications.

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IP Development Engineer

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Key responsibilities

- Development, test and documentation of new IP
- Maintenance and refresh or enhancement of existing IP
- Participation in product planning and definition processes
- Customer interaction during development and roll-out of new products
- Occasional travel as the job requires

Candidate experience

- Proven track record of development with a minimum of two to three years of industry experience, ideally where ASIC or FPGA played a major part in the end product
- Experience of cryptography highly desirable though not essential
- Commercial FPGA design experience essential, ASIC and DSP exposure useful
  - Detailed knowledge of FPGA architectures essential
- Competence in using VHDL and/or Verilog RTL languages essential, scripting languages Tcl, Perl etc and good C software skills useful
- Knowledge and experience in writing software/scripting highly desirable
- Knowledge of leading industry tools such as Modelsim, Aldec, Altera Quartus II, Synplify and Xilinx ISE is highly desirable
- Experience of wired and wireless networking protocols desirable
- Understanding of how to design at the system level useful
- Degree in electronics or equivalent

Candidate profile

The ideal candidate will be a self-motivated individual, equally able to work in a team and independently. This person will be keen to learn, proactive and creative, and always seeking better and more effective methods of design and development.

Developing world class silicon IP requires an intense attention to detail, and the ability to work accurately and efficiently; the ideal candidate will be able to demonstrate clearly these abilities through their past activities.

It is very likely that the candidate already lives and works in the Cambridge area, and will be offered competitive remuneration.

Send your CV and with an introductory note to jobs@heliontech.com.